	Application No.	Applicant(s)
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Notice of Allowability	09/603,184 Examiner	SUZUKI ET AL. Art Unit
•		
	Pierre-Louis Desir	2617
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. X This communication is responsive to <u>01/04/2008</u> .		
2. The allowed claim(s) is/are <u>1-9 and 11-13</u> .		
3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  a) All b) Some* c) None of the:		
1. Certified copies of the priority documents have been received.		
2. Certified copies of the priority documents have been received in Application No		
3. Copies of the certified copies of the priority documents have been received in this national stage application from the		
International Bureau (PCT Rule 17.2(a)).		
* Certified copies not received:		
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		
4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.		
5. CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.		
(a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached		
1) 🗌 hereto or 2) 🔲 to Paper No./Mail Date		
(b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date		
Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).		
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.		
Attachment(s)	6 <b></b>	
1. Notice of References Cited (PTO-892)	5. Notice of Informal P	
2. Notice of Draftperson's Patent Drawing Review (PTO-948)	6. ☐ Interview Summary Paper No./Mail Dat	
3. Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date	7.   Examiner's Amenda	nent/Comment
Examiner's Comment Regarding Requirement for Deposit of Biological Material	8. X Examiner's Stateme	ent of Reasons for Allowance
	9.  Other	

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## **DETAILED ACTION**

## Terminal Disclaimer

1. The terminal disclaimer filed on 01/04/2008 disclaiming the terminal portion of any patent granted on this application which would extend beyond the expiration date of U.S. Patent No. 7,269,237 has been reviewed and is accepted. The terminal disclaimer has been recorded.

## Allowable Subject Matter

2. Claims 1-9, 11-13 are allowed.

The following is an examiner's statement of reasons for allowance: Claim 1 defines a noise cancel circuit for removing noise components in an input audio signal which includes "a timer for starting counting in response to an output signal from the noise detection circuit and generating a switching signal during a predetermined period of time while counting is performed". A selection circuit of claim 1 is further defined in terms of "for selecting one of output signals from a delay circuit and the interpolation circuit in accordance with the switching signal, thereby serving to replace the noise portion of said input audio signal with an output signal from said interpolation circuit according to the output signal from said noise detection circuit and outputting a signal subjected to interpolation processing in its noise portion without adding high frequency components removed from the input audio signal".

Thus, the present invention removes noise, and does so by use of the selection circuit 8 in combination with the timer 7, shown in Fig. 1. In contrast, Tsuji performs synthesis and is incapable of removing a large noise pulse in the manner of the present invention. Tsuji et al.

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performs synthesis by use of the synthesizer circuit 24 shown in Fig. 64 thereof. Claim 1 has been amended to recite a timer in the manner of Fig. I. The timer creates a switching period of time, and when a predetermined switching period of time has passed, the timer automatically returns to its initial state. Tsuji discloses a timer in Fig. 21 thereof, which figure shows a configuration in which an ON/OFF Circuit 20 of Fig. 20 controls an output from a delay circuit to zero. However, in Tsuji, an output signal is always output from an interpolation circuit to the synthesizer circuit, and therefore influences all output results. In the present invention, an output is selected from an interpolation circuit using a predetermined period of time or timing by a switching signal, and, in a normal state, an output from a delay circuit is outputted.

The cited reference does not disclose nor teach the disclosure as recited in independent claim 1.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

## **Conclusion**

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Pierre-Louis Desir whose telephone number is (571) 272-7799. The examiner can normally be reached on Monday-Friday 8:00AM- 5:30PM.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Joseph Feild can be reached on (571) 272-4090. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Pierre-Louis Desir 01/22/2008

SUPERVISORY PATENT EXAMINER